Superior Magnetics Since 1979

CMOB-2

LINE OUTPUT TRANSFORMER
Bifilar Windings

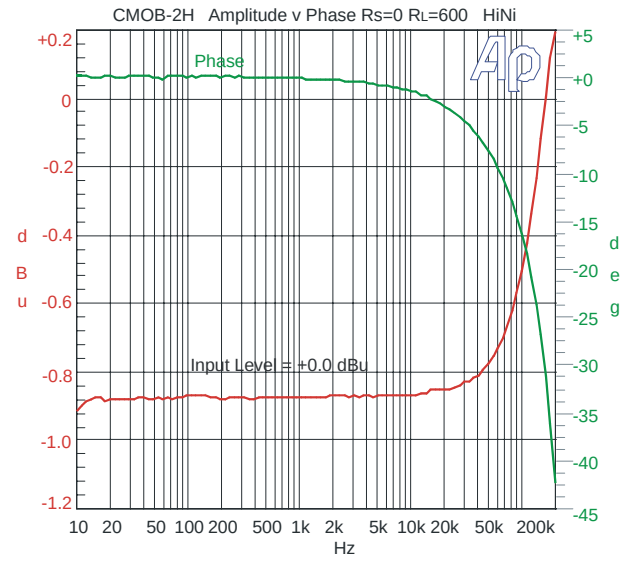
- **Distortion 0.01% typ at 20 Hz, Rs=150Ω**
- **Excellent bandwidth -0.35 dB at 200 kHz**
- **Rs=150Ω 80% Nickel ("HiNi") laminations**
- **+21 dBm at 20 Hz, 1% THD+N Rs≤150Ω**
- **Phase Shift -0.6° at 20 kHz, Rs=150Ω**
- **Low insertion loss**

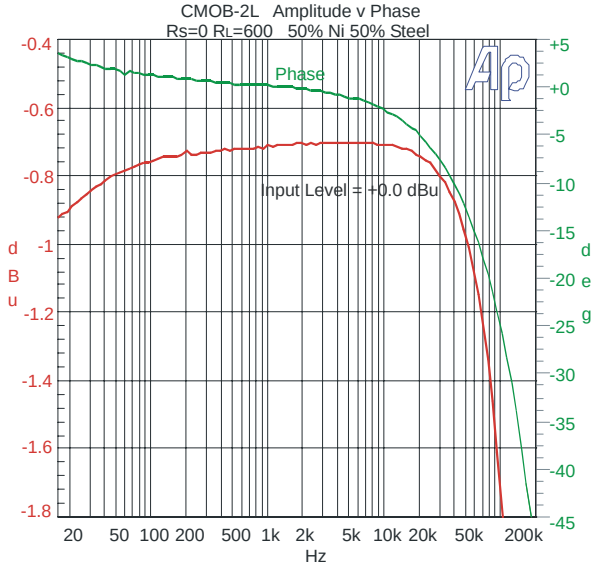
The CineMag CMOB-2 output transformer uses bifilar construction techniques. This results in good coupling between windings as well as excellent bandwidth. It is available both with 80% Nickel ("HiNi") and 50% Nickel/50% Steel laminations. It can be driven with source impedances of up to 600Ω. As with all line driving devices, the amplifier feeding it should be capable of cleanly delivering the power required to reach maximum operating level.

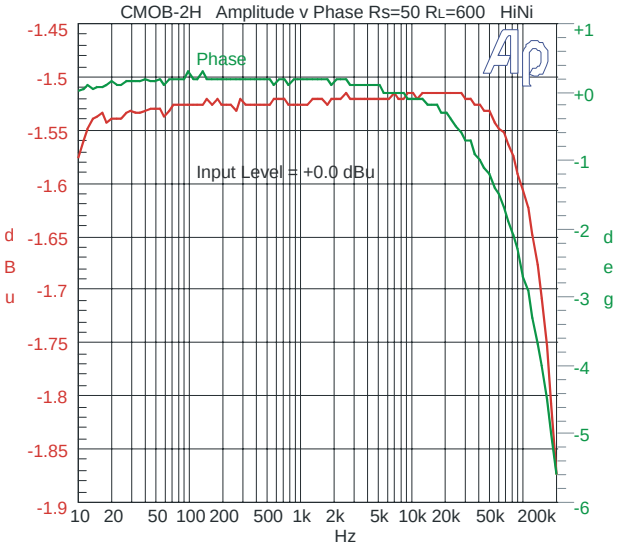
CMOB-2H / CMOB-2L

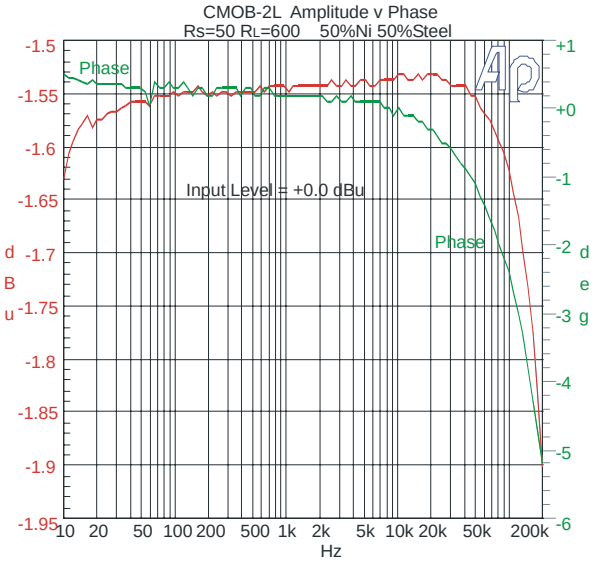
Parameter	Conditions	Typ
Turns Ratio		1 : 1.00
Input Impedance, Zi	20 Hz to 20 kHz, 0 dBu Test Circuit 3	680Ω
Voltage Gain	1 kHz HiNi Core, Rs=0 Test Circuit 1 1 kHz 50% Nickel/50% Steel Core, Rs=0	-0.85 dB -0.70 dB
Distortion (THD+N%)	1 kHz, +4 dBu, Rs=150 HiNi Test Circuit 1 1 kHz, +4 dBu, Rs=150 50%Ni/50% Steel	0.0004% 0.0006%
Max 20 Hz input level	1.0% THD+N, Rs≤150 HiNi Test Circuit 1 1.0% THD+N, Rs≤150 50% Ni 50% Steel	+21 dB +22 dB
Response, ref 1 kHz	20 Hz Rs=150Ω HiNi Test Circuit 1 20 kHz Rs=150Ω HiNi Test Circuit 1 200 kHz Rs=150Ω HiNi Test Circuit 1	-0.08 dB +0.01 dB -0.35 dB
Phase Shift at 20Hz Phase Shift at 20 kHz	Referenced to source generator Test Circuit 1	+2° -0.4°
CMRR	60 Hz Test Circuit 4 per IEEE Std 389-1996 ¶19 1 kHz Test Circuit 4 per IEEE Std 389-1996 ¶19	88 dB 95 dB
Output CMRR	60 Hz Test Circuit 2 1 kHz Test Circuit 2	95 dB 112 dB
Operating Temp Range	Operation and storage	0° C Min 70° C Max

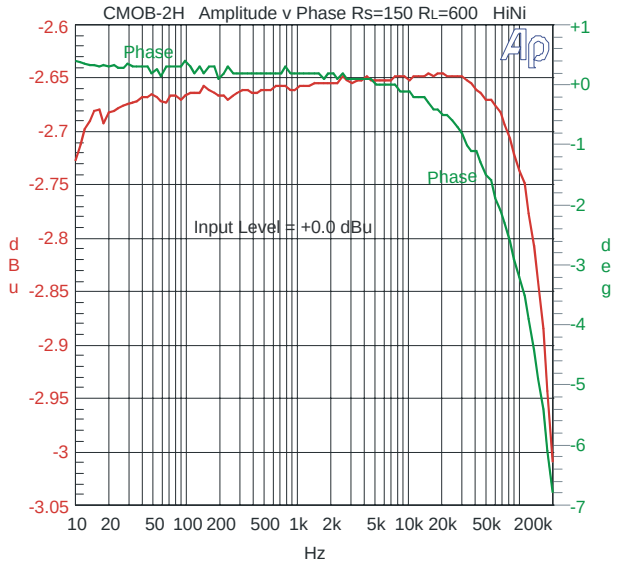
9050 Independence Ave. Canoga Park, California 91304 ☎(818) 993-4644 (818) 993-4604 http://www.cinemag.biz

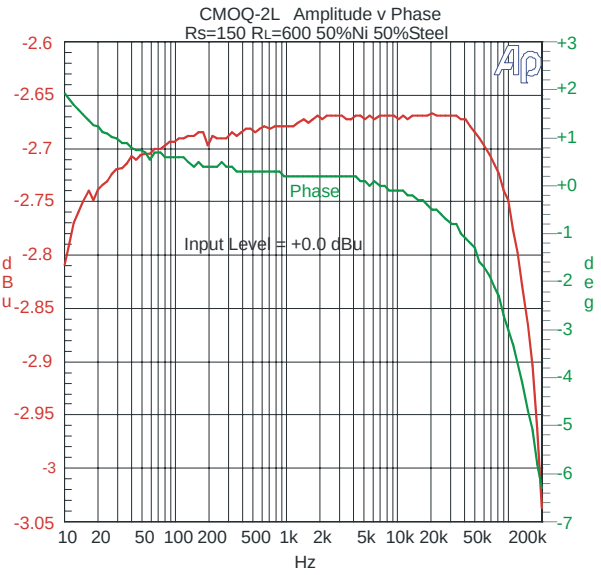


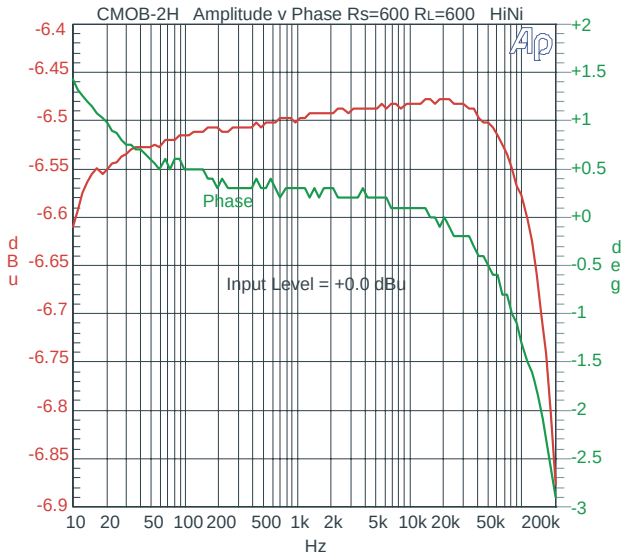


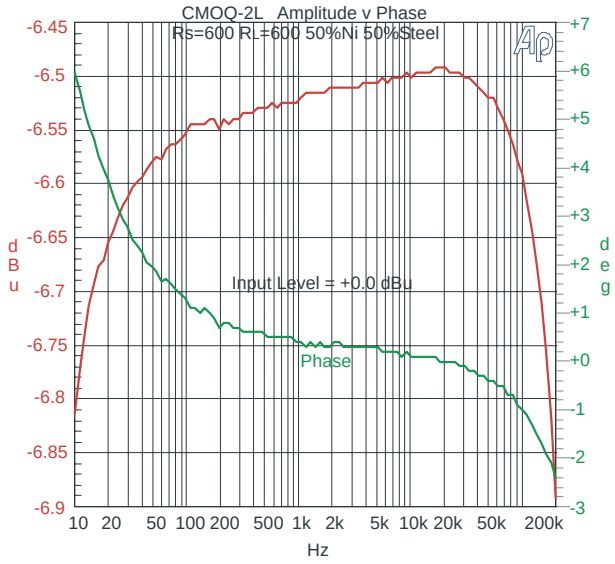


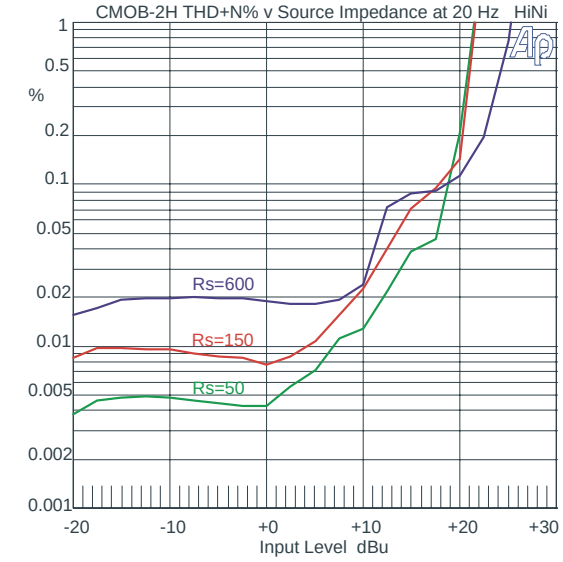


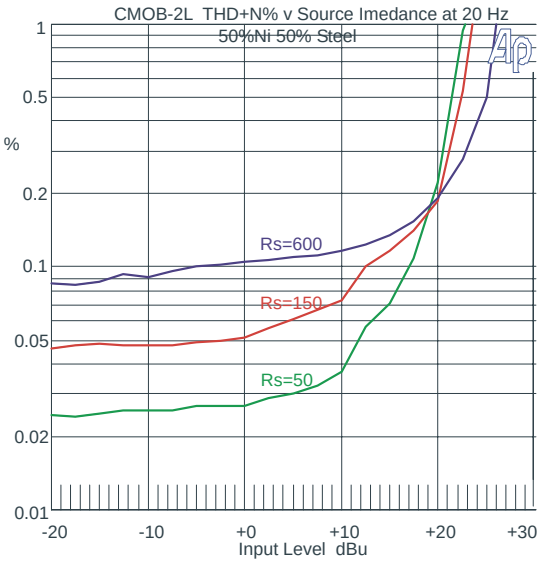


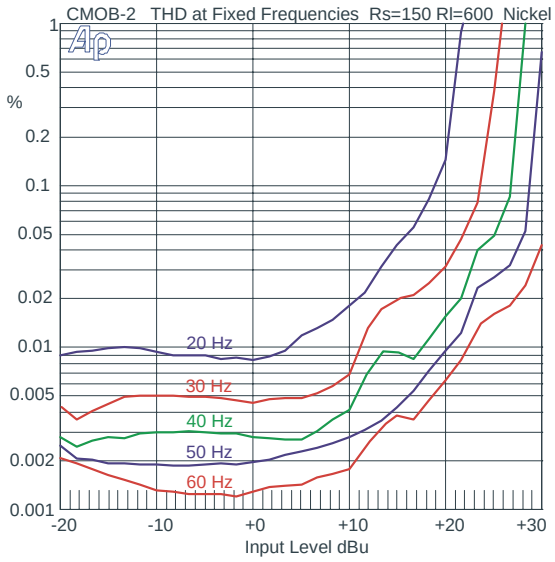


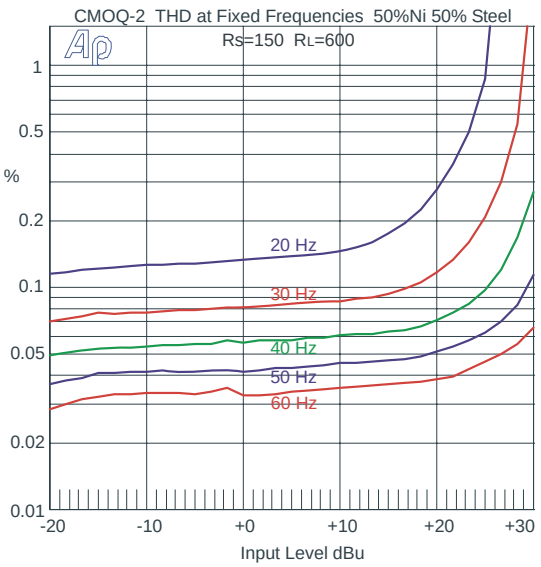




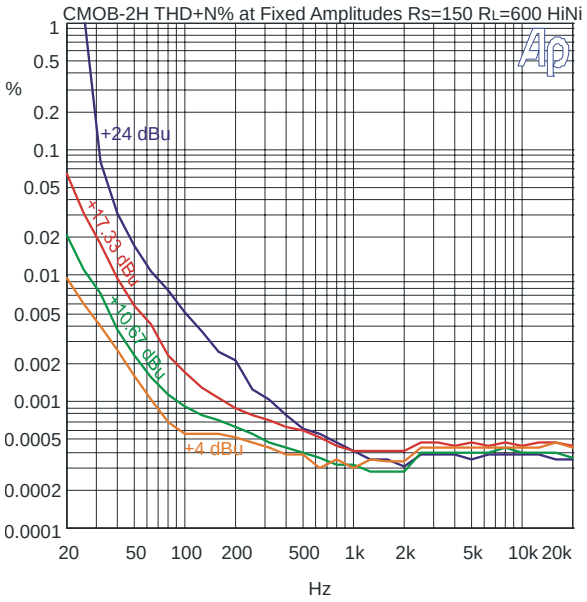


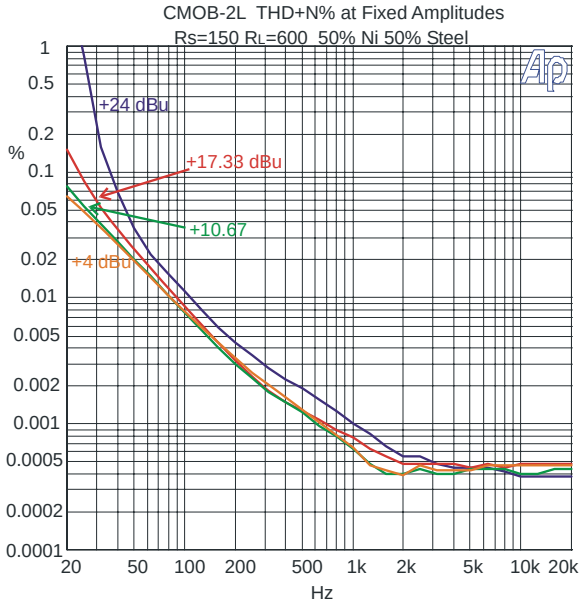


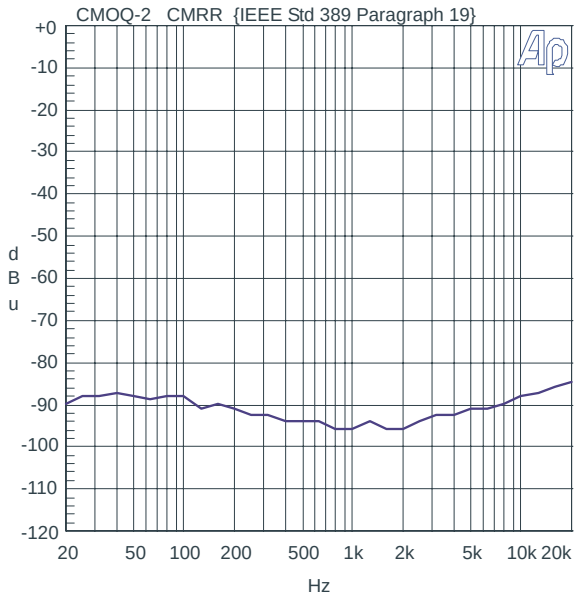


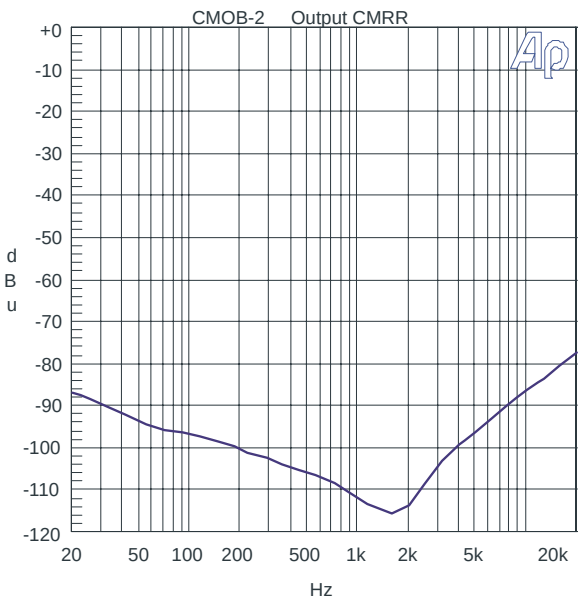


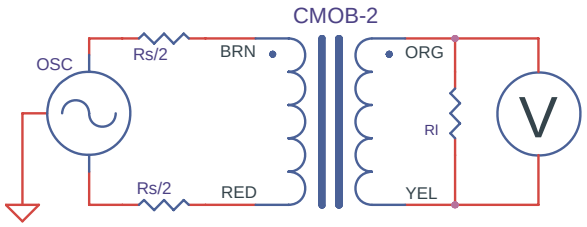
REV B



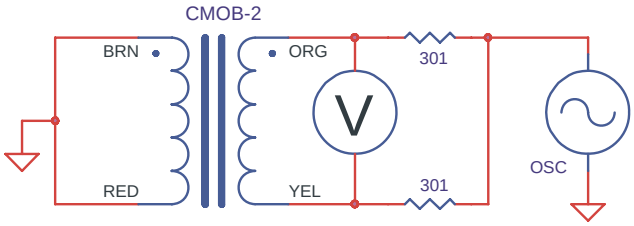




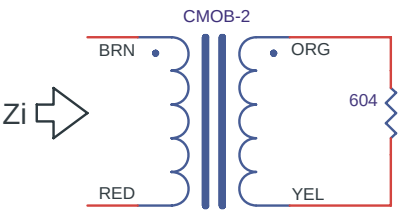




TEST CIRCUIT 1



TEST CIRCUIT 2



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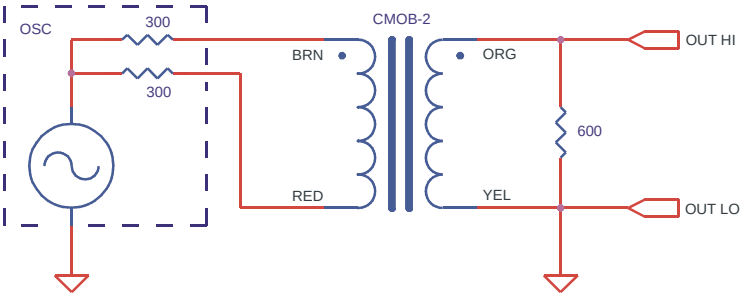

TEST CIRCUIT 4

NOTES:

1. All graphs generated from one (1) randomly chosen device. No statistical averaging or weighting. Data from one sweep.
2. R_L = 604 unless otherwise noted.